

Year & Sem: E2S1	Course Code: EC2703	Lab Name: VLSI	No. of Credits: 2	L 0	T&PS 0	P 2
Experiment-I: Familiarization with VLSI Design flow and VLSI CAD tools Experiment-II: Familiarization with Xilinx ISE Design Software and implementation of Digital designs using Xilinx Schematics Experiment-III: Implementation of combinational and sequential circuits using Gate-level modelling of Verilog HDL Experiment-IV: Implementation of combinational and sequential circuits using data flow modelling of Verilog HDL Experiment-V: Implementation of combinational and sequential circuits using behavioural modelling of Verilog HDL Experiment-VI: Implementation of Finite State Machines using Verilog HDL Experiment-VII: Realization of digital designs using FPGA Experiment-VIII: Familiarization with Mentor Graphics tool : Physical design Experiment-IX: Implementation on Mentor Graphics tool : including layout Experiment-X: ASIC level implementation using Mentor Graphics tool Requirements: 1. Xilinx ISE Software with Modelsim simulation tool 2. FPGA boards 3. Mentor Graphics tool: HEP-1 and HEP-2 packages References/Text Books: 1. 2. Lecture Plan: Unit-I & -II syllabus for MID-I, Unit-III & -IV syllabus for MID-II and Unit-V & -VI syllabus for MID-III examinations. Video Lectures (Web Links): 1. 2. Study Materials (Web Links): 1. 2.						